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Detailed Routing 4.  
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(Chap. 3) Routing  
Geometric Techniques  
Non-Manhattan and  
clockrouting ...

## **Chapter 6 -Detailed Routing**

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compares algorithms  
that are used during  
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circuit design, wherein a geometric chip layout is produced starting from an abstract circuit design. The emphasis is on essential and fundamental techniques, ranging from hypergraph partitioning and circuit placement to timing closure.

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Lienig 1.4 Layout

Layers and Design

Rules • Size rules ,such

as minimum width: The

dimensions of any

component (shape),

e.g., length of a

boundary edge or area

of the shape, cannot be

smaller than given

minimum values.

## **Chapter 1**

*Page 13/20*

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**-Introduction**

Lecture 3: VLSI Design  
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(Part 1)

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The necessary background in graph theory and mathematical optimization is introduced. In the second part, application of different analytical and heuristic techniques to physical design (partitioning,

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placement,  
floorplanning and  
routing) of VLSI circuits  
is studied. We shall

emphasize VLSI design  
issues encountered in  
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explores how algorithms can be used to create a g...

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In integrated circuit design, physical design is a step in the standard design cycle which follows after the circuit design. At this step, circuit representations of the components (devices

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and interconnects) of the design are converted into geometric representations of shapes which, when manufactured in the corresponding layers of materials, will ensure the required functioning of the components.

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